Model 7072T Dual Timing ADC ADC / TDC / SVA



FEATURES

- Three individually selectable modes of operation for each input channel:
 - ADC mode with 500 ns Fixed Conversion Time
 - TDC mode with <<25 ps Time Resolution
 - ADC/SVA (Analog Sampled Voltage Analysis) mode
- Full 8k Conversion Gain and Range
- Dual 10-LED Indicator bar for Instantaneous Dead-Time display, level setting
- Lower Level and Upper Level Discriminator on each ADC input
- Three modes of operation independently selectable for each input channel: Pulse Height Analysis with automatic peak detection / Time-to-Digital conversion and Analog Input Sampling selectable by frontpanel switch
- Simultaneous Single-Channel Output / TDC Monitor
- Designed for easy interfacing to our MCD-2, MCD-2E, SPA-3, MPA-3 Systems, MCA's from other manufacturers (special cable and external ADC interface required) and Computers

DESCRIPTION

The Model 7072T is a unique dual channel device that can operate either as an ultra fast analog-to-digital converter with a fixed conversion time of 500 ns*, a time-to-digital converter (TDC) or an analog SVA. The 7072T is ideally suited for high counting rate applications in energy and time Spectroscopy. The combined ADC/TDC function finds applications in time resolved photon, ion-, neutron-and X-ray imaging.

The built-in Single Channel Analyzer has a separate output on the front panel. This output can be used in the ADC and SVA mode. An analog output is available for monitoring the TDC.

*) Conversion time of ADC. For dead time calculation add time-to-peak and 100 ns transfer time to data buffer Model 7072T Rev3 14112006

In the SVA mode a voltage level or waveform can be sampled when a gate signal is applied.





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SPECIFICATIONS

INPUTS (front panel)

Signal Inputs: BNC-Connector accepts +25mV to +10V (standard / +8V optional) linear pulses, $Z_{in} = 1k$ Ohm, risetime: \geq 100ns to 100 μ s, fall time: 200ns to 100 μ s, dc coupled or a varying voltage level when used in SVA mode.

Gate/Start inputs: BNC-Connector accepts positive TTL for the Gate mode, FAST NIM pulse start input for TDC.

Stop Inputs: BNC-Connector accepts FAST NIM pulse (Stop signal) in TDC mode

OUTPUTS (front panel)

SCA: TTL output in PHA and SVA mode (TTL pulse of approx. 0.5µs duration. One output pulse for each input signal that falls between the ULD and LLD setting), analog signal in TDC mode. The amplitude of the output signal is proportional to the time.

Outputs monitor levels in adjustment mode

LLD: adjustment by up/down pushbutton sets the lower level discriminator. The voltage is displayed on the DEADTIME/LEVEL LED indicator and output on the monitor connector for precise adjustment

ULD: adjustment by up/down pushbutton sets the upper level discriminator. The voltage is displayed on the DEAD-TIME/LEVEL LED indicator and output on the monitor connector for precise adjustment

Front Panel Mode Controls

PHA: Pulse Height Analysis mode.

SVA: Analog Sampled Voltage Analysis

TDC: Time to Digital Converter

Indicators

DEADTIME/LEVEL: Dual 10 LED-bar indicates activity of the ADC. Range 0 to 100% deadtime or actual voltage level indication of ULD and LLD setting

OUTPUTS (rear panel)

Data: 13 bit binary data lines and data transfer commands, 25-pin female D-Sub-connector.

Front Panel Controls by Mode Select pushbutton

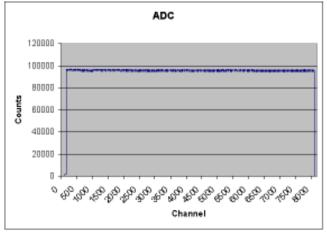
Conversion Range: Push button switch selects quantization of 256, 512, 1k, 2k, 4k, or 8k channels for full scale in

channels for full scale input. **Time Range:** Push button switch selects full scale time range of 50 ns, 100 ns, 200 ns, 500 ns, 1, 2, 5, 10, 20 µs

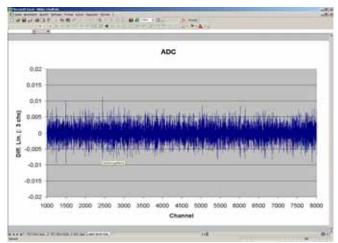
Coincidence/Anticoincidence: Push button switch selects "COINCIDENCE/ANTICOINCIDENCE" mode

ADC Performance	Conv. Range	
	4k	8k
Resolution: 500 ns flattop pulses		<3.4 chan FWHM
Integral Nonlinearity linear fit top 99% of useable ch.	+/-0,16% typical	+/-0,16% typical
Differential Nonlinearity incl. Effects of int.NL	+/-0,7%	+/-1,1%
Differential Nonlinearity for 99% of useable channels	+/-0,5%	+/-0,7%
Counting Rate Capability with MCD-2 MCA	> 1.000.000 events/s	
Peak shift up to 700 kcounts/s SVA mode		+/-0,006%
Peak shift up to 1.000 kcounts/s SVA mode		+/-0,08%
Unused channels	approx. Top 1% of range	

RANGE/MODE: ADC range 256 to 8k in binary steps, TDC range from 50 ns to 20 µs full scale.



PHA, typical noise spectrum



ADC, typical differential linearity plot



PERFORMANCE ADC

Conversion Time: 500 ns fixed conversion time

ADC Deadtime: time-to-peak + 500 ns fixed conversion time + data transfer time of 100 ns to buffer (there could be an additional dead time if the MCA can not accept the data rates generated by the 7072T - this dpends on MCA used)

Typical ADC deadtime for fast rising input pulses is less

than 2 µs per event using FAST ComTec MCA's

Gain stability: better than 50 ppm/°C

Baseline stability: 50µV/°C

PERFORMANCE TDC

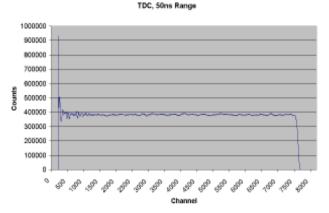
Time range: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s, 2 μ s,

5 μs, 10 μs, 20 μs

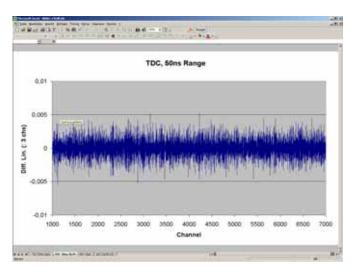
Useable times: >12..15 ns to approx. 95% of selected

range

Conversion range: 8192



TDC, typical noise spectrum (50 ns range)



TDC, typical differential linearity plot (50 ns range)

TDC Performance at 8k conversion range	
Resolution: @ 50 ns time range	<4.1 chan FWHM
Integral Nonlinearity 50 ns range, linear fit	+/-0,12% typical
Differential Nonlinearity 50 ns range	+/-0,5%
Differential Nonlinearity for 99% of useable channels	+/-0,4%
Counting Rate Stability up to 100 kcounts/s	+/-0,003%

General

Temperature range: 0°C to +50°C storing

Power Requirements

+24V, 100mA, -24V, 90 mA, +12V, 150mA, -12V, 200

mA, + 6V, 700mA, -6V, 430 mA

Physical

Size: single width NIM module (1.35 x 871 inches;

3.43 x 22.13 cm) as per TID - 20893 (rev.)

Shipping weight: 1.8 kg (net 1.0 kg)



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