

# Pulse Amplifier

# A275



The A275 is a high performance hybrid differential op-amp developed as a pulse amplifier for spaceborne nuclear instrumentation.

Its low power dissipation (15 mW), high slew rate (100 V/ $\mu$ s), and low input noise (4 nV/ $\sqrt{\text{Hz}}$ ), make it ideal for use in a wide range of op-amp applications. The A275 is packaged in a standard 14-pin hybrid DIP.

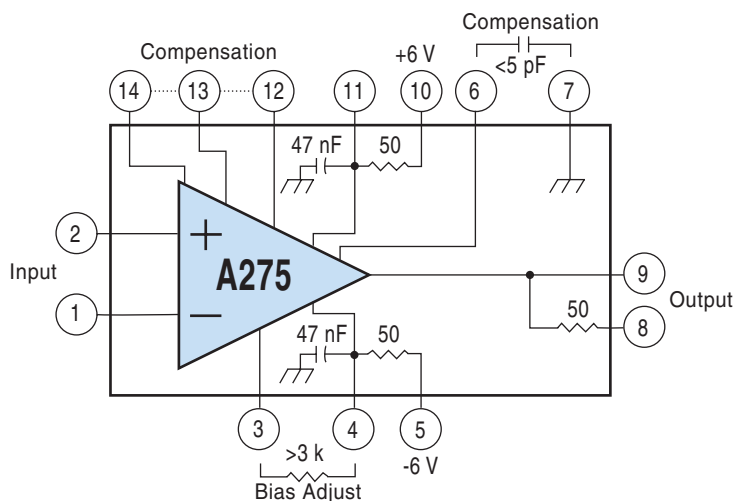
## Features

- Power 15 mW
- Slew rate 100 V/ $\mu$ s
- Input noise 4 nV/ $\sqrt{\text{Hz}}$
- Stable DC operation
- 200 MHz gain-bandwidth product
- High reliability screening
- Unity gain stable

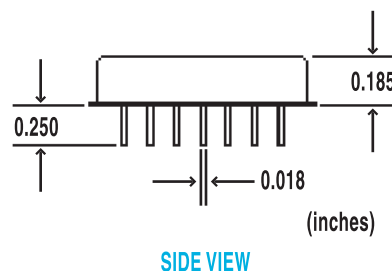
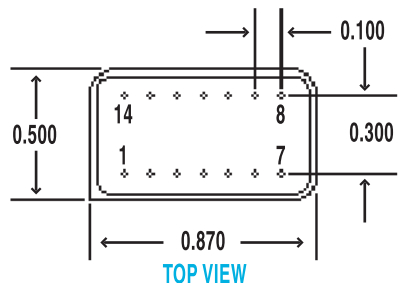
## Applications

- Space instrumentation
- Portable instrumentation
- Nuclear instrumentation
- Precision active filter design
- Pulse shaping

## CONNECTION and PACKAGE DIAGRAMS



| PIN        | FUNCTION  |
|------------|---|
| 1          | Inverting input   |
| 2          | Non-inverting Input   |
| 3          | Bias adjust to Pin 4, ( $R > 3 \text{ kohm}$ )                      |
| 4          | $-V_s$ direct   |
| 5          | $-V_s$ through 50 ohms  |
| 6          | Compensation to Pin 7, ( $C = 0-5 \text{ pF}$ )                     |
| 7          | Case and Ground   |
| 8          | Output through 50 ohms  |
| 9          | Output direct   |
| 10         | $+V_s$ through 50 ohms  |
| 11         | $+V_s$ direct   |
| 12, 13, 14 | Compensation: Leave open for gain $< 10$ ;<br>Short for gain $> 10$ |



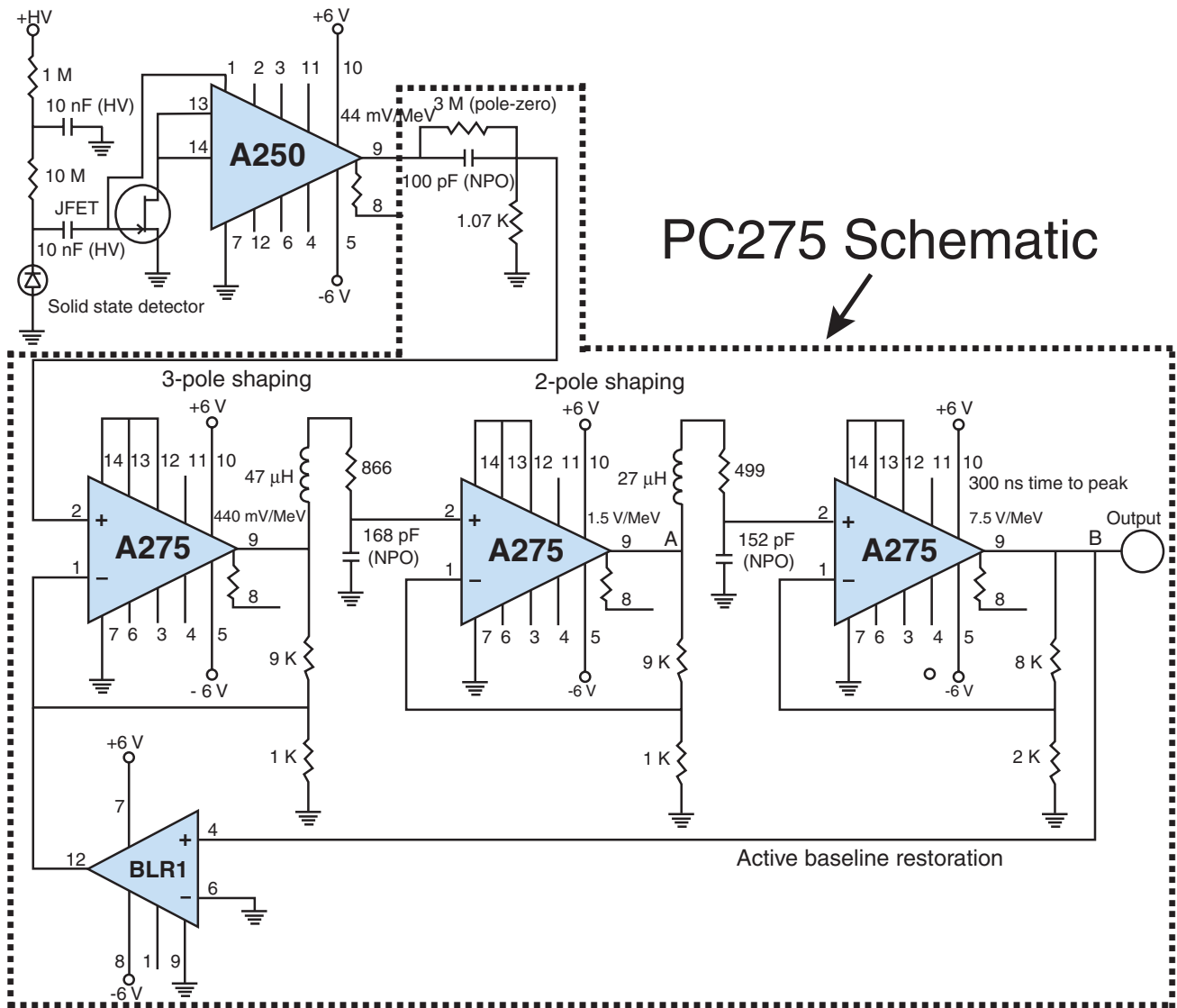
## AMPTEK INC.

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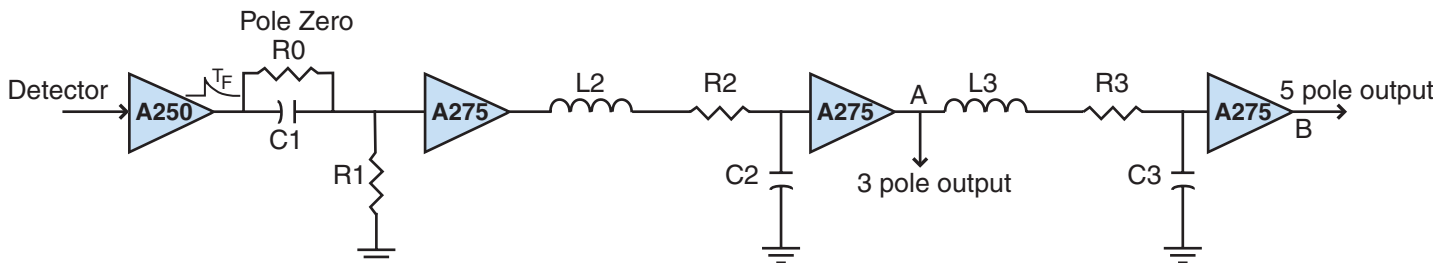
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## APPLICATION NOTES

### The A250 Connected to a Solid State Detector with 5 pole shaping and active baseline restoration



### General Case for 3 and 5 Pole Response for Different Peaking Times ( $T_p$ )

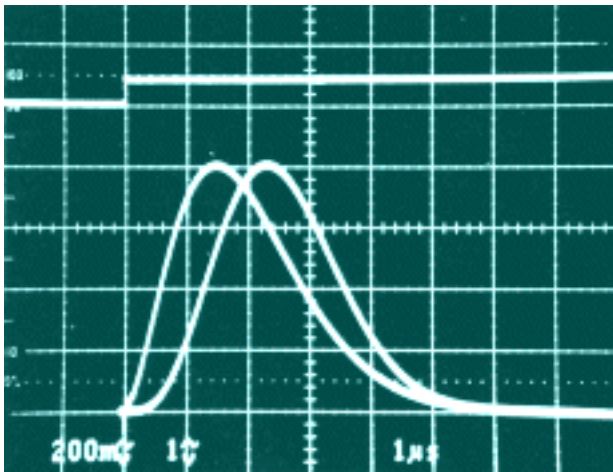


|                       |                                  |   |
|-----------------------|----------------------------------|---|
| $R1 = 1.07 \text{ K}$ | $C1 = 360 \text{ pF} \times T_p$ | $L2 = 150 \text{ } \mu\text{H} \times T_p$                              |
| $R2 = 834$            | $C2 = 592 \text{ pF} \times T_p$ | $L3 = 82 \text{ } \mu\text{H} \times T_p$                               |
| $R3 = 457$            | $C3 = 564 \text{ pF} \times T_p$ | $T_p = \text{Desired Peaking Time in } \mu\text{s for 5 Pole Response}$ |
| $R0 = T_p / C1$       |                                  | $\text{Peaking Time at the 3 Pole Output} = (2/3) T_p$                  |

Note: Trim C2 for optimum baseline recovery at "A," then trim C3 for optimum baseline recovery at "B."

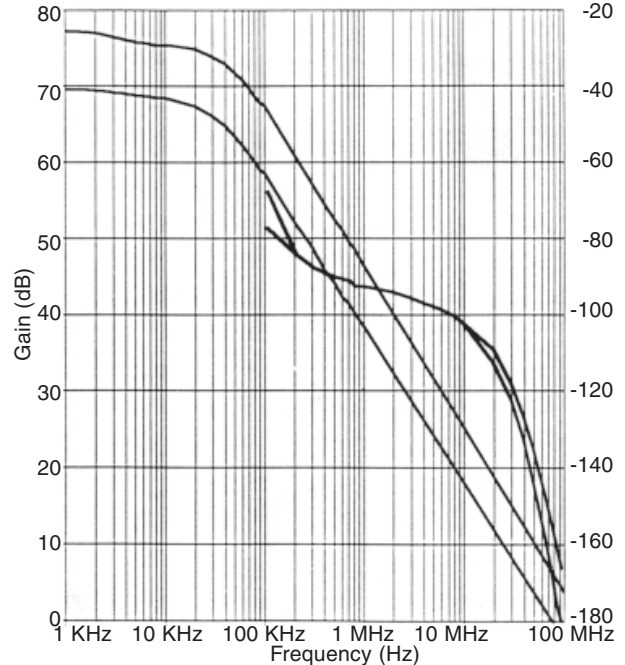
## OPERATING NOTES

### PC275 Input/Output Waveforms



Upper: Input Pulse  
First Output (A): 3 Pole  
Second Output (B): 5 Pole

### A275 Gain/Phase

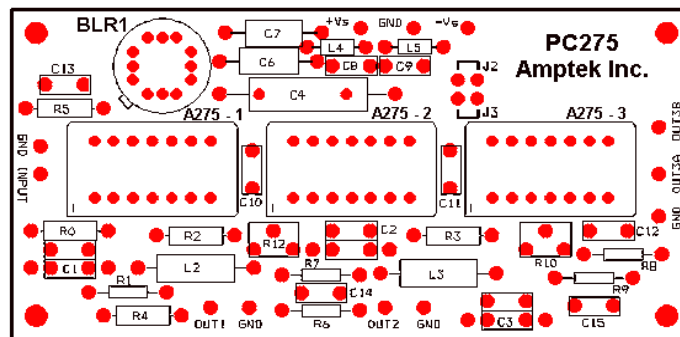


### Circuit Layout Considerations

Due to the high bandwidth of the A275, care should be taken in circuit layout. In general, ground plane construction is recommended. Input and output lines should be well separated and in most cases shielding will be necessary. Grounding the physical case of the A275 (in addition to Pin 7) to the ground plane can be used to avoid oscillations or electronic pick-up.

The A275 can be tested as a shaping amplifier by using the circuit shown in the next page. The PC-275 Test Board (shown below) accommodates three (3) A275s and a BLR1 and produces a 5 pole pulse with 1  $\mu$ s risetime (2.3  $\mu$ s peaking time).

### PC-275 TEST BOARD for the A275



Actual Size

Dimensions: 3.5in x 1.75in (88.9mm x 44.45mm)

### A275F

The A275FC and A275FN are high density versions of the Amptek A275 and feature a Single In-line Package (SIP).

Please visit our web site at [www.amptek.com](http://www.amptek.com) for complete details.

For more information visit [www.amptek.com](http://www.amptek.com)

## A275 SPECIFICATIONS

### Absolute Maximum Ratings

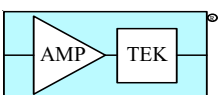
|   |                  |
|---|------------------|
| Supply Voltage                              | ±8 V             |
| Input Voltage                               | ± V <sub>S</sub> |
| Operating Temperature                       | -55°C to +125°C  |
| Storage Temperature                         | -65°C to +150°C  |
| Lead Temperature Range (Soldering, 10 sec.) | 300°C            |

### Electrical Characteristics V<sub>S</sub> = ±6V, T<sub>A</sub> = +25°C

| PARAMETER                            | SYMBOL           | CONDITIONS    | MIN  | TYP   | MAX  | UNITS  |
|--------------------------------------|------------------|---------------|------|-------|------|--------|
| Input Offset Voltage                 | V <sub>OS</sub>  |               |      | 2     | 5    | mV     |
| Input Offset Current                 | I <sub>OS</sub>  |               |      | 0.1   | 0.6  | μA     |
| Input Bias Current                   | I <sub>B</sub>   |               |      | 1.5   | 4    | μA     |
| Input Capacitance                    | C <sub>IN</sub>  |               |      | 4     |      | pF     |
| Differential Input Resistance        | R <sub>IN</sub>  |               |      | 44    |      | k Ohm  |
| Common-Mode Input Resistance         | R <sub>IN</sub>  |               | 5    | 8     |      | M Ohm  |
| Common-Mode Rejection Ratio          | CMRR             |               | 90   | 95    |      | dB     |
| Common-Mode Input Range              | IVR              |               |      | ±4.5  |      | V      |
| Power Supply Rejection Ratio         | PSRR             |               |      | 60    |      | dB     |
| Large-Signal Voltage Gain            |                  |               |      |       |      |        |
| @5 kHz                               | A <sub>LFC</sub> | Compensated   | 66   | 68    |      | dB     |
| @5 kHz                               | A <sub>LF</sub>  | Uncompensated | 73   | 76    |      | dB     |
| @10 MHz                              | A <sub>HFC</sub> | Compensated   | 18   | 21    |      | dB     |
| @10 MHz                              | A <sub>HF</sub>  | Uncompensated | 26   | 28    |      | dB     |
| Pulse Risetime (A <sub>V</sub> = 10) | t <sub>rc</sub>  | Compensated   |      | 15    | 22   | ns     |
|                                      | t <sub>r</sub>   | Uncompensated |      | 9     | 15   | ns     |
| Output Voltage Swing                 | V <sub>OP</sub>  | Positive      | +4.5 | +4.7  |      | V      |
|                                      | V <sub>ON</sub>  | Negative      |      | -4.5  | -4.5 | V      |
| Open Loop Output Resistance          | R <sub>O</sub>   |               |      | 750   |      | Ohm    |
| Output Short-Circuit Current         | I <sub>OSC</sub> | Source        |      | 11    |      | mA     |
|                                      |                  | Sink          |      | -4    |      | mA     |
| Slew Rate                            | S <sub>RP</sub>  | Positive      | 65   | 100   |      | V/μs   |
|                                      | S <sub>RN</sub>  | Negative      | 35   | 57    |      | V/μs   |
| Input Noise Voltage Density          | e <sub>n</sub>   |               |      | 4     |      | nV/√Hz |
| Supply Current                       | I <sub>S</sub>   |               | ±1.1 | ±1.25 | 1.4  | mA     |
| Power Consumption                    | P <sub>d</sub>   |               |      | 15    |      | mW     |

### General

|  |  |
|--|--|
| Package  | 14 Pin Hybrid DIP  |
| Test Board   | PC275  |
| Screening<br>(see web site: <a href="http://www.amptek.com/hybrids.html">www.amptek.com/hybrids.html</a> ) | Amptek High Reliability Screening (Standard)<br>Option 1<br>Option 2                         |
| Other Package<br>(see web site: <a href="http://www.amptek.com/A275f.html">www.amptek.com/A275f.html</a> ) | A275FC or A275FN High-density versions of the Amptek A275 in a Single In-line Package (SIP). |



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