(Feb. 2007)

General Description

Cremat's CR-113 is a single channel charge sensitive preamplifier module intended for use with various types of radiation detectors. The CR-113 is one of a series of four charge sensitive preamplifiers offered by Cremat, which differ from each other most notably by their gain. A guide to selecting the best charge sensitive preamplifier for your application can be found at our web site: http://cremat.com. As with all Cremat's preamplifier modules, the CR-113 is small (less than one square inch in area), allowing for compact multichannel detection systems to be constructed using a modular design.

Detector coupling

The CR-113 can be used either in a *direct coupled* (DC) mode, or an *AC coupled* mode. If the detector current exceeds 30 μA, it is recommended that an AC coupled mode be used to prevent the resulting DC offset of the preamplifier output from saturating. Low frequency detector current (e.g. 'dark' current, or leakage current) produces an offset in the preamplifier output voltage at a rate of 70 mV per μA. The use of AC coupling also is useful in improving the counting rate capability of the preamplifier. A schematic diagram of an AC-coupled charge sensitive preamplifier detection circuit can be found at **http://cremat.com/CSP_app_notes.htm**
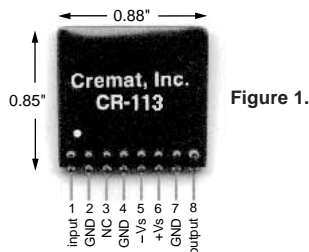

Figure 1.

Package Specifications

The CR-113 circuit is contacted via an 8-pin SIP connection (0.100" spacing). Leads are 0.020 inches wide. Pin 1 is marked with a white dot for identification.
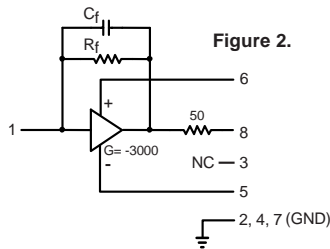

Figure 2.

Equivalent circuit diagram

Figure 2 above shows a simplified equivalent circuit diagram of the CR-113, which is a single stage amplifier. Pin numbers corresponding with the CR-113 preamplifier are shown. R_f (68 kΩ) and C_f (750 pF) are the feedback resistor and capacitor respectively.
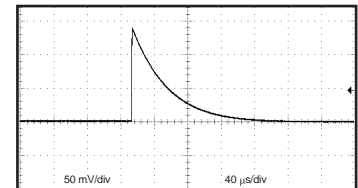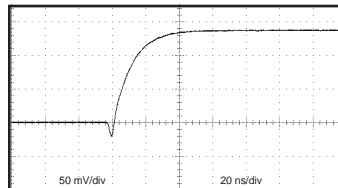
Output waveform

Charge sensitive preamplifiers are used when radiation is detected as a series of pulses, resulting in brief bursts of current flowing into or out of the preamplifier input. Depending on the type of detector, this burst of current may be very brief (<1ns) or as long as a few μs. For an idealized detection current pulse taking the form of a delta function, the detected charge (time integral of the input current) will ideally take the form of a step function.

The output waveform of an actual charge sensitive preamplifier will of course have a non-zero rise time: for the CR-113 this figure is approximately 20 ns. Furthermore, capacitance at the preamplifier input (i.e. detector capacitance) will further slow the rise time at a rate of 0.25 ns / pF.

Keep in mind the output rise time will also be limited by the speed of the detector. For example, the detection current pulse from a CsI(Tl)/PMT scintillation detector has a duration of approximately a couple μs, so the expected rise time of the charge sensitive preamplifier output will be at least that long.

The output waveform of the CR-113 using a capacitively-coupled fast square wave pulser at the input is shown below to the left. At long time domains, the output decays due to the discharge of the feedback capacitor through the feedback resistor, with an RC time constant of 50 μs. This decay of the output waveform is also shown below, to the right.

| 50 mV/div | 20 ns/div |

| 50 mV/div | 40 μs/div |

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Specifications

Assume temp =20 °C, V_s = ±6.1V, unloaded output

	CR-113	units
Preamplification channels	1	
Equivalent noise charge (ENC)*		
ENC RMS	24,000	electrons
	4	femtoCoul.
ENC slope (noise increase per input cap.)	27	elect. RMS / pF
Gain	1.5	mV / picoCoul.
Rise time **	20	ns
Decay time constant	50	μs
Unsaturated output swing	-3 to +3	volts
Maximum charge detectable per event	1.3×10^{10}	electrons
	2.1	nanoCoul.
Power supply voltage (V_s)		
maximum	$V_s = \pm 13$	volts
minimum	$V_s = \pm 6$	volts
Power supply current (pos)	5.5	mA
(neg)	5.5	mA
Power dissipation	70	mW
Operating temperature	-40 to +85	°C
Output offset	-40 to -60	mV
Output impedance	50	ohms

* Measured with input unconnected, using Gaussian shaping amplifier with time constant =1 μs. With a detector attached to the input, noise due to the detector capacitance, detector leakage current, and dielectric losses will add to this figure.

** Pulse rise time (defined as the time to attain 90% of maximum value) has a linear relationship with input capacitance. Value cited in the table assumes zero added input capacitance. To calculate pulse rise time for practical situations, use the equation: $t_r = 0.25 C_d + 20$ ns, where t_r is the pulse rise time in ns, and C_d is the added capacitance (e.g. detector capacitance) in pF.